OCXO2525BM-FD-10MHz LVTTL-3122

25.4x25.4x12.7mm 10MHz OCXO

2550 Gray Falls Dr., Suite#128, Houston, TX, 77077 USA TEL: 1-281-870-8822 EMAIL: Sales@DynamicEng.com

Features and Benefits

Frequency range: 10MHz Supply voltage: 3.3V Steady state: 1.3W Max Output waveform: LVTTL

Frequency stability vs. operating temperature: ±10ppb

Aging: ±50ppb per year

Phase noise@10KHz: -156dBc/Hz Operating temperature: -30°C to +70°C

Size:25.4x25.4x12.7mm

Typical Applications

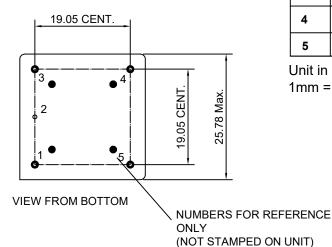
Small Cell, Portable Telecommunication Device Test and Instrumentation Synthesizer, Digital switch, Reference Timing Circuit

Description

OCXO2525BM-FD-10MHz_LVTTL-3122 is designed for applications where exceptional frequency stability and timing is required. It has both excellent temperature performance and short-term stability. These characteristics make it an excellent choice for timing applications.

Mechanical Drawing & Pin Connections

12.7 **GLASS STANDOFF** (4PLACES) 0.8DIA.PIN(5 PLACES)



Drawing No: MD160042-4

PIN Function

Pin	Function
1	R.F. OUTPUT
2	GND
3	Control Votage
4	Reference Voltage
5	Supply Voltage

Unit in mm 1mm = 0.039 inches

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Specifications

Specification Operational Frequency RF Output Waveform Level High Level Load Duty Cycle Rise/Fall time Spurious Electrical Frequency Adjustment (PIN = "V	Sym F _{nom}	Condition @+1.65V	Min. +2.4	Typ. 10 Rectangular LVTTL	Max.	MHz	
RF Output Waveform Level High Level Low Level Load Duty Cycle Rise/Fall time Spurious	RL		+2.4	10 Rectangular		MHz	
Waveform Level High Level Low Level Load Duty Cycle Rise/Fall time Spurious			+2.4		•		
Level High Level Low Level Load Duty Cycle Rise/Fall time Spurious			+2.4		·		
High Level Low Level Load Duty Cycle Rise/Fall time Spurious			+2.4	LVTTL			
Low Level Load Duty Cycle Rise/Fall time Spurious			+2.4				,
Load Duty Cycle Rise/Fall time Spurious						V	,
Duty Cycle Rise/Fall time Spurious					+0.4	V	,
Rise/Fall time Spurious				15pF			
Spurious			45	50	55	%	
		10% to 90%			6	ns	
Flactrical Fraguency Adjustment (DIM = ")					-60	dBc	
Liectrical Frequency Aujustinent (PIN = "V	CO INPU	Γ")					
Tuning Range –		VCO @ Min. Voltage			-0.5	ppm	Referenced to frequency at nominal Center
		VCO @ Max. Voltage	+0.5			ppm	Voltage
Control Voltage			0	1.4	2.8	V	
Slope				positive		<u> </u>	
Linearity			-10		+10	%	
Input Impedance			100			Kohm	
Reference Voltage (PIN = "Reference Voltage"	age")					, , , , , , , , , , , , , , , , , , ,	
Voltage			2.7	2.8	2.9	V	
Load			9			Kohm	
Power Supply							
Supply Voltage	Vs	_	3.135	3.3	3.465	V	
Steady state		+25°C			1.3	W	
Current		@ turn on			1000	mA	
Frequency Stability							
Versus Operating Temperature Range		ref to +25°C			±10	ppb	
Initial Frequency Accuracy		@ +25±1°C; after turn on power 15±1 minutes; <=90 days following date code; VCO Input voltage @ Center Voltage ±0.001V			±0.1	ppm	
Versus supply voltage		±5% change			±0.5	ppb	
Versus Load		±5% change			±0.5	ppb	
Short Term		9			0.05	ppb/s	Root Allan variance
Aging		Per day, at time of shipment			±0.5	ppb	
Aging Per Day		after 30 days			±0.5	ppb	
Aging 1st Year					±50	ppb	
Aging 10 Years					±0.3	ppm	
Warm-up		In 10 minutes @25±1°C			±10	ppb	Reference to 1 hour
		1Hz		-95	-90	dBc/Hz	
		10Hz		-125	-120	dBc/Hz	
Dhaga Najas		100Hz		-140	-135	dBc/Hz	
Phase Noise		1kHz		-148	-145	dBc/Hz	
		10kHz		-156	-155	dBc/Hz	
		100kHz		-158	-155	dBc/Hz	
Environmental, Mechanical Conditions							
	-30°C to +						
	-55°C to +105°C						
	MIL-STD-202, Method 103 Test Condition A; 95% RH @ +40°C, non-condensing,240 hours						
	MIL-STD-202, Method 201; 0.06" total p-p, 10-55Hz						
Shock (non-operating)	MIL-STD-	202, Method 213, test cond	dition J; 30g	,,11ms, half-	sine		