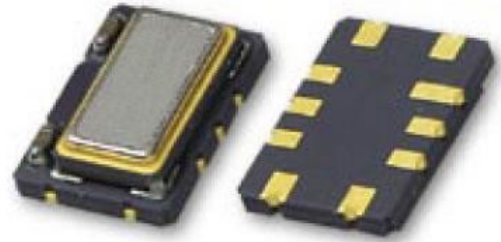


**Features**

Frequency 12.8 MHz  
 7mm x 5mm x 1.85mm ceramic SMD  
 +/- 4.6 ppm total stability over 20 years  
 CMOS output  
 Tri-state Enable / Disable Function  
 +/- 0.50 ppm from -40 to +85 centigrade degree

**Picture of Part**



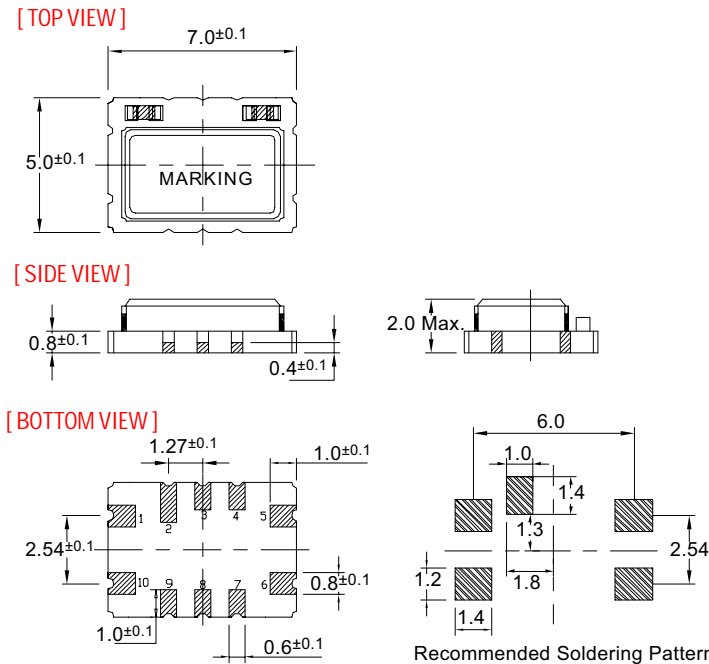
**Typical Applications**

Base stations  
 10 G-bit ethernet  
 SONET  
 GSM, CDMA, 3G, and 4G cellular

**Description**

The TCXO3404 family offers low noise compensation techniques combined with aggressive conditioning processes resulting in outstanding long term stability, tightly distributed performance parameters, and superior long term reliability

**Physical Dimensions**



**Pin Connections**

Pad	Function
1	VCON : VC-TCXO NC : TCXO
2	NC
3	NC
4	NC
5	GND
6	CMOS/ Clipped Sinewave Output
7	NC
8	NC
9	Tri-State Control*
10	VDD

**Specification**

TCXO Specification	Sym.	Condition	Value			Unit	Note
			Min.	Typ.	Max.		
<b>Operational Frequency Range</b>	$f_0$			12.800		MHz	
Load					15	pF	
	H - level voltage	$V_H$		0.9Vcc		V	
	L - level voltage	$V_L$			0.1Vcc	V	
	Rise & Fall time					ns	
	Duty cycle			45		55	%
<b>Power supply</b>							
Voltage	$V_{cc}$		3.135	3.300	3.465	V	5.0 V option available
Current consumption	$I_{cc}$				6.0	mA	square wave
<b>Frequency stability</b>							
vs. temperature		-40 °C to +85 °C, ref 25 °C	-0.500		+0.500	ppm	
vs. 5% change in supply voltage		ref Vcc typ.	-0.300		+0.300	ppm	
Tolerance at 25C			-2.000		+2.000	ppm	Frequency 1 hr after reflow
<b>SSB Phase noise @ 12.8 MHz CMOS typical</b>		10 Hz			-90	dBc/Hz	
		100 Hz			-120		
		1 KHz			-140		
		10 KHz			-148		
Total Tolerance	Over 20 years	Projected after 30 days operation	-4.600		+4.600	ppm	
<b>Environmental, mechanical conditions.</b>							
Operating temperature range		<b>-40 °C to +85 °C maximum range available that is standard</b>					
Storage temperature range		<b>-55 °C to +125 °C</b>					
Tri-state		Output OFF : Less than 30% of Vcc					
Enable / Disable		Output ON : Greater than 70% of Vcc ; or do NOT connect					