#### OCXO1490AX

High stability Low phase noise OCXO

### **Features and Benefits**

Frequency range: 10-120MHz Supply voltage: 3.3V or 5.0V Steady state: 1.5W Typ

Output waveform: Sinewave or CMOS/TTL

Frequency stability vs. operating temperature: ±20.0ppb

Aging: ±100ppb per year

Phase noise@1KHz: -145dBc/Hz
Operating temperature: -40°C to +85°C

Size:14.4x9.4x11.4mm

## **Typical Applications**

Cellular Base Stations Instrumentation Microwave Applications Radar reference

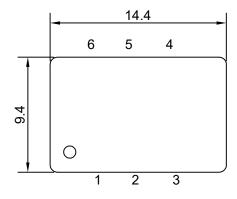
## **Description**

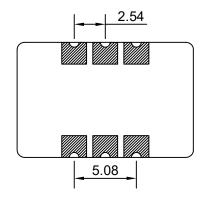
The OCXO1490AX is designed for applications where exceptional frequency stability and timing is required. It has both excellent temperature performance and short-term stability. These characteristics make it an excellent choice for timing applications.

# **Mechanical Drawing & Pin Connections**

**Drawing No:** 

MD240008-1





#### Pin Connections

Pin	Function
1	Control Voltage/N.C.
2	N.C.
3	GND
4	Output
5	N.C.
6	Supply Voltage

Unit in mm 1mm = 0.0394 inches



# Dynamic Engineers Inc.

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# **Specifications**

Oscillator				Value			
Specification	Sym	Condition	Min.	Typ.	Max.	Unit	Note
Frequency Range	F <sub>nom</sub>		10		120	MHz	
RF Output							
Signal Waveform				CMOS	S/TTL		
Load	$R_L$			15		pF	
H-Level Voltage	$V_H$		90%Vcc			V	
L- Level Voltage	$V_L$				10%Vcc	V	
Duty Cycle			45	50	55	%	
Rise/Fall time					5	ns	
Signal Waveform				Sine	wave		
Level			45	+9		dBm	
Load			45	50	55	ohm	
Harmonics					-40 -70	dBc	
Spurious					-70	dBc	
Power Supply		1	4.75	5.0	5.05		
Supply Voltage	Vcc		4.75	5.0	5.25	V	
		To initial talerance	3.13	3.3	3.47	min	
Warm-up Time	T <sub>up</sub>	To initial tolerance Steady state, +25°C		1.5	3	min W	
Power Consumption		Warm-up		1.5	3.5	W	
Frequency Adjustment Range		waiiii-up			3.3	VV	
rrequency Adjustment Range		T	1				
Electronic Frequency Control (EFC)			±0.5 or ±1.0			ppm	
			_				
EFC voltage	V <sub>c</sub>		0	Vcc/2	Vcc	V	
Input Impedance				100		<b>k</b> Ω	
Linearity				10		%	
EFC Slope				positive			
Frequency Stability			1	00 50			
Versus Operating Temperature Range		Max-Min/2		±20, ±50 or ±100		ppb	
Initial Tolerance		+25°C±1°C			±100	ppb	
Versus supply voltage		±5% change		±2		ppb	
Versus load		±5% change		±2		ppb	
Aging Per Day		after 30 days of			±1.0	ppb	
Aging 1 <sup>st</sup> Year		operation			±100	ppb	
Allan Variance		1s		5		e-11	
				Sine	CMOS		
		10Hz		-120	-120	dBc/Hz	
SSB Phase noise (10MHz) (Typical value)		100Hz		-140	-140	dBc/Hz	
COD I Mase Moise (Tolvill 12) (Typical Value)		1kHz		-145	-145	dBc/Hz	@+25°C
		10kHz		-155	-150	dBc/Hz	
		100kHz		-155	-155	dBc/Hz	
				Sine	CMOS		
		10Hz		-90	-90	dBc/Hz	
SSB Phase noise (100MHz) (Typical		100Hz		-120	-120	dBc/Hz	@+25°C
value)		1kHz		-145	-140	dBc/Hz	
		10kHz		-155	-145	dBc/Hz	
Environmental Machanical Conditi		100kHz		-155	-150	dBc/Hz	
			2001 250				
Environmental, Mechanical Conditions	000 17	000 0000 + 7000 44					
Operating temperature range		0°C, -20°C to +70°C, -40	)°C to +85°C	,			
Operating temperature range Storage temperature range	-55°C to +	+100°C		,			
Operating temperature range Storage temperature range Mechanical shock	-55°C to - MIL-STD-	-100°C 202 Method 213 Test Co	ondition C				
Operating temperature range Storage temperature range	-55°C to + MIL-STD- MIL-STD-	+100°C	ondition C				