TCXO3225BL-48MHz-A-V IÌTP: Á/ÔÝUÁ

Features and Benefits

48MHz Frequency
3.3V Supply voltage
LVCMOS Output waveform
±2.5ppm Stability Vs -40°C to +85°C

Typical Applications

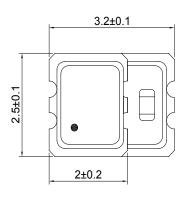
Frequency reference for real time clocks (RTCs)
Portable instruments
Timing synchronization for networks, servers, hubs, routers and switches
Smart metering, data loggers
GPS receivers. Telematics

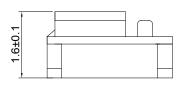
Description

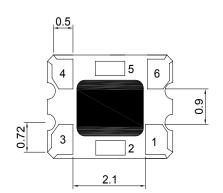
TCXO3225BL-48MHz-A-V is designed for applications where exceptional frequency stability and timing is required. It has both excellent temperature performance and short-term stability. These characteristics make it an excellent choice for timing applications.

Mechanical Drawing & Pin Connections

Drawing No: MD160046-1







Pin Function

1 Voltage Control

2 Output Enable

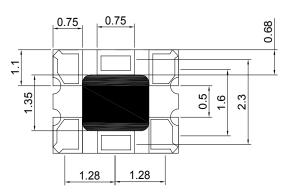
3 GND

4 Output

5 N.C.

6 Vcc

Pin Connection



Unit in mm 1mm = 0.0394 inches

Dynamic Engineers Inc.

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Specifications

Oscillator Specification	Sym	Condition	Min.	Value	Max.	Unit	Note
Operational Frequency	F _{nom}		IVIIII.	Typ. 48	IVIAX.	MHz	
RF Output	1 nom			40		IVII IZ	
Signal Waveform	1		LVCMOS				
Load	R _L		15pf				
H-Level Voltage	V _H		90% Vcc				
L- Level Voltage	VL		10% Vcc				
Duty Cycle			45	50	55	%	
Rise and fall time			1.5 nS. (Typical), 3.0 nS. (max.) Tr / Tf: 10% ↔ 90%				
Otant un time			waveform				
Start up time			5 m sec. (max.)				
Power Supply		50/	1	0.0	1		
Supply Voltage	V _{cc}	±5%		3.3		V	
Current consumption			1	26	1	mA	
Current with output disabled				18		mA	
Frequency Stability Versus Operating Temperature Range	1	-40°C to +85°C	1	.2.5	1	nnm	
versus Operating Temperature Range	-	-40°C 10 +65°C	1.4	±2.5		ppm	
Initial Calibration Tolerance			±1.0 ppm. max. at +25°C±2°C. (at the shipment)				
Versus supply voltage		±5% change			±0.2	ppm	
Versus load		±10% change			±0.2	ppm	
Versus Reflow		1 reflow and measured 24 hours afterwards			±1.0	ppm	
Aging 1 st Year					±2.0	ppm	25°C
Aging 10 Year					±10	ppm	25°C
Storage Temperature			-55°C to +150°C				
Control Voltage Function on Pad 1			33				
Control Voltage Center and Range			+	1.5V ± 1.0	V		
Frequency Pulling Range			± 8 ppm min.				
Linearity			± 1 % typical. ± 10% max.				
Transfer Function			Positive Transfer				
Absolute Voltage			4.0 V max.				
Input Impedance			770 KΩ typical.				
Harmonics			-5.0 dBc max.				
Output Enable Function on pad 2							
OE Control on Pad 2			0.7% of VDD (min.) or no connection to enable output. LVCMOS / LVTTL level. 0.3% of VDD (max.) to disable output (high impedance).				
0				LVCMOS / LVTTL level. 200 nS. Max. / 50 nS. Max.			
Output Enable Time / Disable Time							
Integrated Phase Jitter			, ,	1.5 pS typical (12 KHz to 20 MHz)			
			< 400 fS	< 400 fS (1.875 KHz to 21 MHz)			